

WEST Search History

DATE: Monday, March 14, 2005

Hide? Set Name Query

Hit Count

DB=PGPB,USPT; PLUR=NO; OP=ADJ

<input type="checkbox"/>	L64	158 and 153	0
<input type="checkbox"/>	L63	158 and 154	5
<input type="checkbox"/>	L62	158 and 137	0
<input type="checkbox"/>	L61	154 and 156	7
<input type="checkbox"/>	L60	156 and 158	5
<input type="checkbox"/>	L59	153 and 154	11
<input type="checkbox"/>	L58	712/248.ccls.	172
<input type="checkbox"/>	L57	712/227.ccls.	526
<input type="checkbox"/>	L56	712/229.ccls.	269
<input type="checkbox"/>	L55	712/32.ccls.	355
<input type="checkbox"/>	L54	712/37.ccls.	152
<input type="checkbox"/>	L53	712/39.ccls.	70
<input type="checkbox"/>	L52	atmel.asn. and spi	7
<input type="checkbox"/>	L51	L6 and L50	1
<input type="checkbox"/>	L50	(overwrit\$4 or over-writ\$4 or eras\$3) same L49	39
<input type="checkbox"/>	L49	test\$3 same calibrat\$3 same program\$5	3597
<input type="checkbox"/>	L48	(overwrit\$4 or over-writ\$4 or eras\$3) same L47	1
<input type="checkbox"/>	L47	test\$3 same calibrat\$3 same microcontroller\$1	286
<input type="checkbox"/>	L46	(overwrit\$4 or over-writ\$4) same L27	0
<input type="checkbox"/>	L45	(overwrit\$4 or over-writ\$4) same (L40 or L41)	1
<input type="checkbox"/>	L44	wright.in. and wick.in.	0
<input type="checkbox"/>	L43	L40 and L41	0
<input type="checkbox"/>	L42	L40 and L24L24	0
<input type="checkbox"/>	L41	load\$3 with calibration program\$1	39
<input type="checkbox"/>	L40	load\$3 with test program\$1	613
<input type="checkbox"/>	L39	calibration program\$3 with eras\$3	1
<input type="checkbox"/>	L38	test program\$3 with eras\$3	53
<input type="checkbox"/>	L37	5428770.uref.	11
<input type="checkbox"/>	L36	microcontroller\$1 and L35	49
<input type="checkbox"/>	L35	L33 and L34	814
<input type="checkbox"/>	L34	714/\$.ccls.	30469

<input type="checkbox"/>	L33	702/\$.ccls.	27073
<input type="checkbox"/>	L32	dual purpose interface	5
<input type="checkbox"/>	L31	test program\$1 same calibration program\$1	10
<input type="checkbox"/>	L30	L29 and microcontroller\$1	3
<input type="checkbox"/>	L29	(702/119).ccls. and (702/120).ccls.	25
<input type="checkbox"/>	L28	L27 same (manufactur\$3 or fabricat\$3)	7
<input type="checkbox"/>	L27	test\$3 same calibrat\$3 same microcontroller\$1 same mode	60
<input type="checkbox"/>	L26	test\$3.clm. and L18	35
<input type="checkbox"/>	L25	test\$3.clm. and L22	5
<input type="checkbox"/>	L24	test\$3.clm. and chip.clm. and L22	0
<input type="checkbox"/>	L23	test\$3.clm. and manufactur\$3.clm. and L22	0
<input type="checkbox"/>	L22	treat.xa.	66
<input type="checkbox"/>	L21	test\$3.clm. and manufactur\$3.clm. and L18	1
<input type="checkbox"/>	L20	test\$3.clm. and chip.clm. and L18	2
<input type="checkbox"/>	L19	test\$3.clm. and chip.clm. L18	0
<input type="checkbox"/>	L18	treat.xp.	605
<input type="checkbox"/>	L17	manufactur\$3 same L8	51
<input type="checkbox"/>	L16	(L11 or L13) and usb	4
<input type="checkbox"/>	L15	L7 and L11	25
<input type="checkbox"/>	L14	L7 and L13	8
<input type="checkbox"/>	L13	5596734.uref.	50
<input type="checkbox"/>	L12	5596734.pn.	1
<input type="checkbox"/>	L11	5495593.uref.	31
<input type="checkbox"/>	L10	5495593.pn.	1
<input type="checkbox"/>	L9	test\$3 same L8	26
<input type="checkbox"/>	L8	L6 with L7	457
<input type="checkbox"/>	L7	re-program\$8 or reprogram\$8	25519
<input type="checkbox"/>	L6	microcontroller\$1 or micro-controller\$1	47098
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L5	5467286.pn.	1
		<i>DB=EPAB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L4	5467286.pn.	0
<input type="checkbox"/>	L3	embedded.ti. and sun.in. and processor.ti.	1
		<i>DB=DWPI; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L2	embedded.ti. and sun.in. and processor.ti.	0
<input type="checkbox"/>	L1	embedded.ti. and sun.in.	36

END OF SEARCH HISTORY